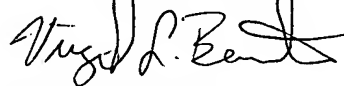


Remarks

After entry of this Preliminary Amendment, claims 1-20 are pending in the application, with 1, 10, and 16 being the independent claim. Claims 11-15 are sought to be amended. These changes are not believed to include any new matter, and their entry is respectfully requested.

Respectfully submitted,

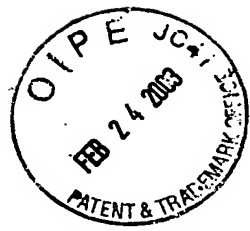
STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.



Virgil L. Beaton
Attorney for Applicant
Registration No. 47,415

Date: 2/25/03

1100 New York Avenue NW, #600
Washington, D.C. 20005-3934
(202) 371-2600



Applicant: Keshab K. Parhi
Appl. No. 10/055,910

Version with markings to show changes made

Please substitute the following claim 11 for the pending claim 11:

11. (ONCE AMENDED) The digital logic circuit of claim 10, wherein the digital logic circuit forms part of a transceiver.

Please substitute the following claim 12 for the pending claim 12:

12. (ONCE AMENDED) The digital logic circuit of claim 10, wherein the digital logic circuit forms part of a computer processing system.

Please substitute the following claim 13 for the pending claim 13:

13. (ONCE AMENDED) The digital logic circuit of claim 10, wherein the first delay device and the second delay device each comprise at least one data flip-flop.

Please substitute the following claim 14 for the pending claim 14:

14. (ONCE AMENDED) The digital logic circuit of claim 10, wherein the digital logic circuit forms part of a communications system operating at a data rate of at least 3 gigabits per second.

Please substitute the following claim 15 for the pending claim 15:

15. (ONCE AMENDED) The digital logic circuit of claim 10, wherein the digital logic circuit forms part of a decision feedback equalizer.

SKGF_DC1:98714.1